

1/4

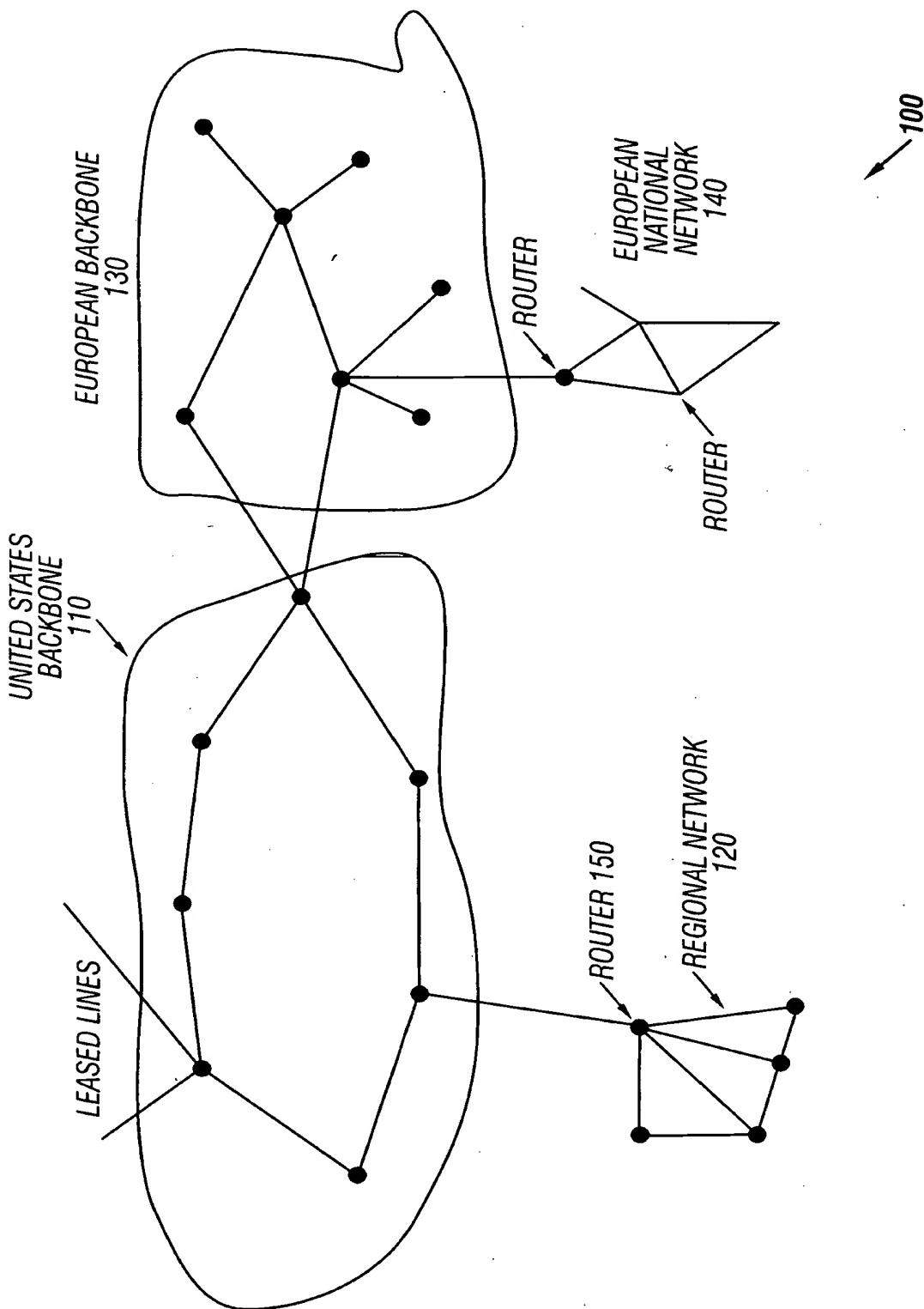


FIG. 1

2/4

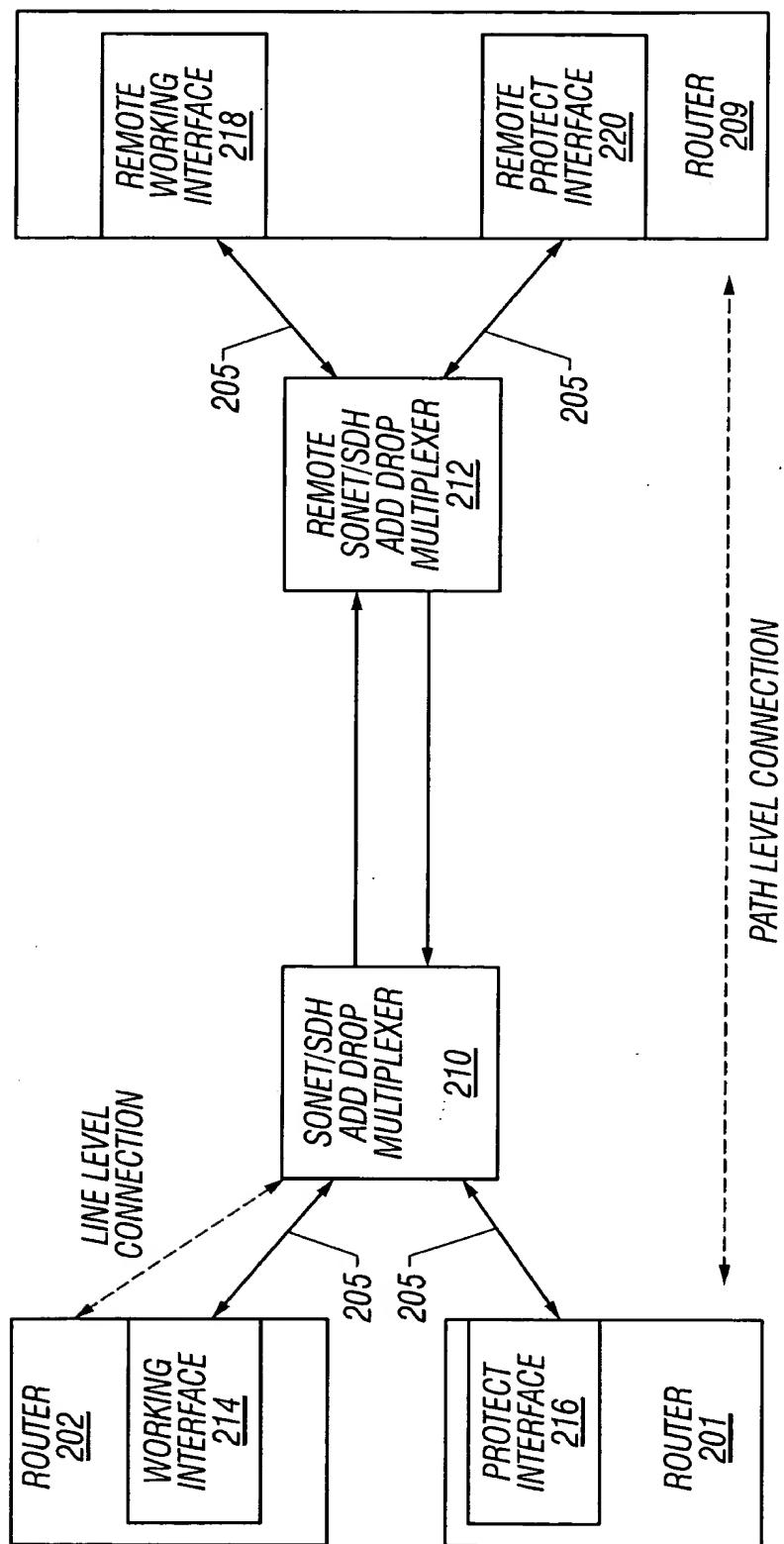


FIG. 2

PATH LEVEL CONNECTION

3/4

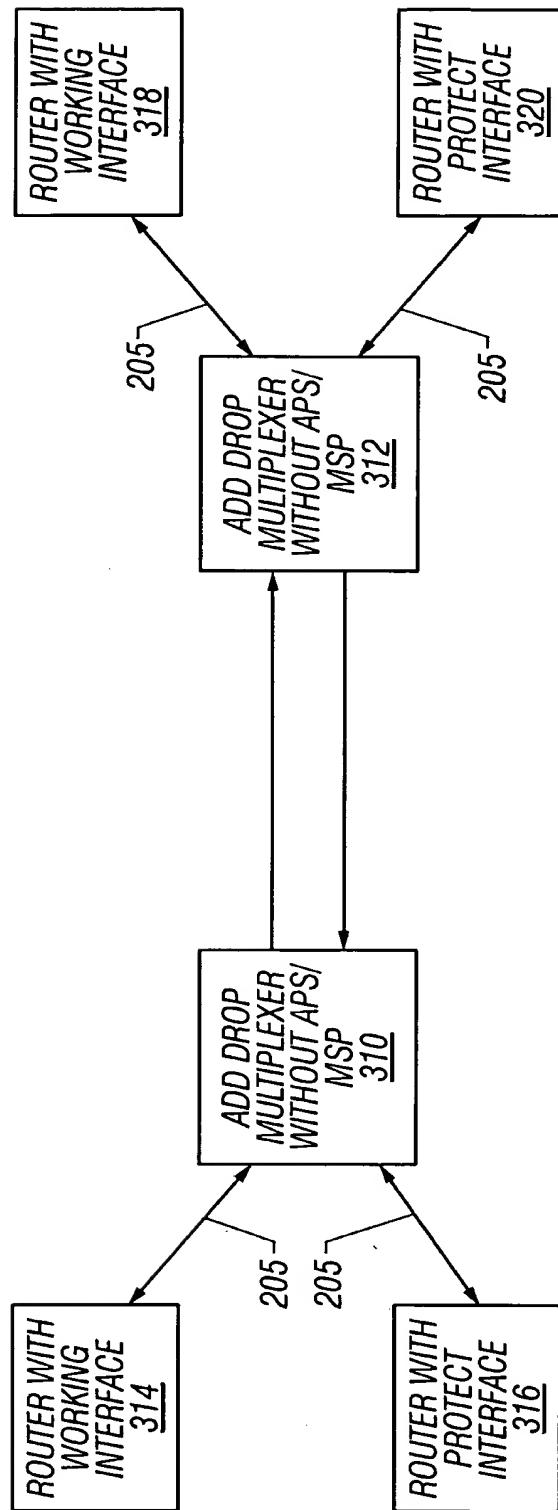


FIG. 3

4/4

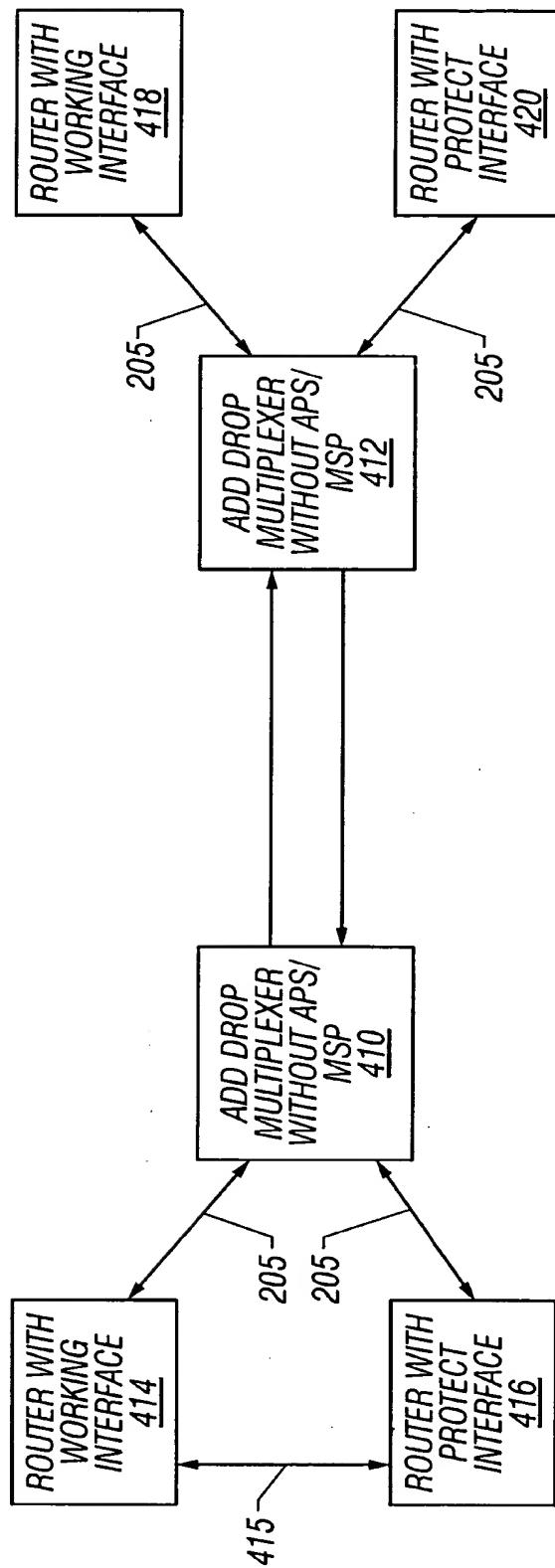


FIG. 4